



Master of Technology VLSI System Design

Fee Structure Applicable for Intake 2026
M.Tech VLSI System Design

FEE STRUCTURE
A4 Landscape

Sem	Heads	Graduation % >=80	Graduation % >=75 and <80	Graduation % >=70 and <75	Graduation % >=70 and <65	Graduation % <65
1st Year Payment Options	Tuition Fee Per Semester	77000	77000	77000	77000	77000
	Scholarship	8400	8300	4200	2000	0
	Yearly Payment (5% Discount)	130340	134330	138320	142500	146300
	Half Yearly Payment	68600	70700	72800	75000	77000
2nd Year Payment Options	Tuition Fee Per Semester	79000	79000	79000	79000	79000
	Scholarship	8400	8300	4200	2000	0
	Yearly Payment (5% Discount)	134140	138130	142120	146300	150100
	Half Yearly Payment	70600	72700	74800	77000	79000
Total Fee (Per Year Installment)		264480	272460	280440	288800	296400
One Time Payable	Admission Fee			10000		
	Enrolment Fee			5000		
	Caution Money (Refundable)			3000		
	Yearly Payment			18500		
Other Charges^				9750		
Scholarship for Wards of Defence personnel: 5%						
25% of all seats are available for scholarship, scholarship will be given on first come first serve basis.						
^ Includes Examination, Library, Insurance, Student Welfare, Sports, Online student info, PDP etc)						